

JCS60 U.S. PTO
09/739078
12/13/00

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| 156 | 272.2 |
| Class | ISSU |

PATENT NUMBER
6475326

6475326

U.S. UTILITY Patent Application

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| O.I.P.E. TR 40 CH SCANNED Q.A. | PATENT DATE NOV 05 2002 |
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|------------------------------|------------|--------------|-------------------|------------------|-------------------|
| APPLICATION NO. 09/739078 | CONT/PRIOR | CLASS 156 | SUBCLASS 272.2 | ART UNIT 1733 | EXAMINER Haran |
|------------------------------|------------|--------------|-------------------|------------------|-------------------|

APPLICANTS

Harald Gross

TITLE

Anodic bonding of a stack of conductive and dielectric layers

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PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | |
|------------------------------|--|----------|--|--------------------|--|-----------------------------------|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | |
| CLASS | | SUBCLASS | | CLASS | | SUBCLASS (ONE SUBCLASS PER BLOCK) | |
| 156 | | 272.2 | | 156 | | 273.9 274.4 | |
| INTERNATIONAL CLASSIFICATION | | | | 65 59.1 | | | |
| B32B | | 31/00 | | | | | |
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10/4/02 Formal Drawings (4 sheets) set 01 05/14/02

| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|--|--|-------------|------------|---|----------------------|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| | 4 | 11 | 2 | 34 | 1 |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | J. T. Haran 6/14/02 (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED 6/18/02 | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ | Michael W. Ball Primary Patent Examiner Technology Center 1700 6/18/02 (Primary Examiner) (Date) | | | ISSUE FEE Amount Due \$1580.00 Date Paid 9-19-02 | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | Hensley 6-20-02 (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |

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